

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	176	((119/837) or (606/164)).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2007/12/14 09:45
L2	3	L1 and @pd>"20050202"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2007/12/14 09:46
L3	3	"beak" and "bird" and "energy" and ("tongue" or "throat") and "aperture"	US-PGPUB	OR	OFF	2007/12/14 09:48
L4	9	"beak" and "bird" and "energy" and ("tongue" or "throat") and "aperture"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2007/12/14 09:54
L5	46	"452"/\$.ccls. and "beak"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	OR	OFF	2007/12/14 09:55

Most frequently occurring classifications of patents returned  
from a search of 10702369 on Dec 14 , 2007

Original Classifications

8	606/164
3	438/297
2	119/464
2	119/715
2	438/241
2	119/72.5

Cross-Reference Classifications

4	257/506
3	438/440
3	257/E21.258
2	257/E21.339
2	257/E21.564
2	257/648
2	438/298
2	219/384
2	438/294

Combined Classifications

8	606/164
4	438/297
4	257/506
3	438/440
3	119/464
3	119/715
3	257/E21.258
2	257/E21.339
2	257/E21.564
2	119/537
2	438/241
2	257/648
2	438/298
2	219/384
2	438/452
2	119/468
2	438/294
2	119/72.5

## 10702369\_LIST.txt

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PLUS Search Results for S/N 10702369 Searched Dec 14, 2007.

The Patent Linguistic Utility System (PLUS) is a USPTO automated search system for U.S. Patents from 1971 to present. PLUS is a query-by-example search system which produces a list of patents that are most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

20050115521

7232450

20040143278

4343310

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5250298	69
5346421	69
5393693	69

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Titles of most frequently occurring classifications of patents returned  
from a search of 10702369 on Dec 14 , 2007

8 606/164 (8 OR, 0 XR)  
Class 606 SURGERY  
606/1 .INSTRUMENTS  
606/163 ..Means for debeaking or dehorning animals  
606/164 ...By electrical or thermal application

4 438/297 (3 OR, 1 XR)  
Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
438/142 .MAKING FIELD EFFECT DEVICE HAVING PAIR OF ACTIVE REGIONS  
SEPARATED BY GATE STRUCTURE BY FORMATION OR ALTERATION OF SEMICONDUCTIVE ACTIVE  
REGIONS  
438/197 ..Having insulated gate (e.g., IGFET, MISFET, MOSFET, etc.)  
438/294 ...Including isolation structure  
438/297 ....Recessed oxide formed by localized oxidation (i.e.,  
LOCOS)

4 257/506 (0 OR, 4 XR)  
Class 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE  
DIODES)  
257/499 .INTEGRATED CIRCUIT STRUCTURE WITH ELECTRICALLY ISOLATED  
COMPONENTS  
257/506 ..Including dielectric isolation means

3 438/440 (0 OR, 3 XR)  
Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
438/400 .FORMATION OF ELECTRICALLY ISOLATED LATERAL SEMICONDUCTIVE  
STRUCTURE  
438/439 ..Recessed oxide by localized oxidation (i.e., LOCOS)  
438/440 ...Including nondopant implantation

3 119/464 (2 OR, 1 XR)  
Class 119 ANIMAL HUSBANDRY  
119/416 .CONFINING OR HOUSING  
119/452 ..Animal display or open work enclosure (e.g., cage, etc.)  
119/459 ...Bird cage  
119/464 ....With feeding or watering means

3 119/715 (2 OR, 1 XR)  
Class 119 ANIMAL HUSBANDRY  
119/712 .ANIMAL CONTROLLING OR HANDLING (E.G., RESTRAINING,  
BREAKING, TRAINING, SORTING, CONVEYING, ETC.)  
119/713 ..Bird  
119/714 ...Body worn  
119/715 ....Head carried

3 257/E21.258 (0 OR, 3 XR)  
Class 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE  
DIODES)  
257/E21.001 .PROCESSES OR APPARATUS ADAPTED FOR MANUFACTURE OR  
TREATMENT OF SEMICONDUCTOR OR SOLID-STATE DEVICES OR OF PARTS THEREOF (EPO)  
257/E21.002 ..Manufacture or treatment of semiconductor device (EPO)  
257/E21.04 ...Device having at least one potential-jump barrier or  
surface barrier, e.g., PN junction, depletion layer, carrier concentration layer  
(EPO)  
257/E21.085 ....Device having semiconductor body comprising Group IV  
elements or Group III-V compounds with or without impurities, e.g., doping materials  
(EPO)  
257/E21.211 .....Treatment of semiconductor body using process other  
than deposition of semiconductor material on a substrate, diffusion or alloying of

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impurity material, or radiation treatment (EPO)  
257/E21.214 ....To change their surface-physical characteristics or shape, e.g., etching, polishing, cutting (EPO)  
257/E21.24 ....To form insulating layer thereon, e.g., for masking or by using photolithographic technique (EPO)  
257/E21.258 .....Using masks (EPO)

2 257/E21.339 (0 OR, 2 XR)  
Class 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)  
257/E21.001 .PROCESSES OR APPARATUS ADAPTED FOR MANUFACTURE OR TREATMENT OF SEMICONDUCTOR OR SOLID-STATE DEVICES OR OF PARTS THEREOF (EPO)  
257/E21.002 ..Manufacture or treatment of semiconductor device (EPO)  
257/E21.04 ...Device having at least one potential-jump barrier or surface barrier, e.g., PN junction, depletion layer, carrier concentration layer (EPO)  
257/E21.085 ....Device having semiconductor body comprising Group IV elements or Group III-V compounds with or without impurities, e.g., doping materials (EPO)  
257/E21.328 .....Radiation treatment (EPO)  
257/E21.331 .....With high-energy radiation (EPO)  
257/E21.334 .....Producing ions for implantation (EPO)  
257/E21.335 .....In Group IV semiconductor (EPO)  
257/E21.339 .....Of electrically inactive species in silicon to make buried insulating layer (EPO)

2 257/E21.564 (0 OR, 2 XR)  
Class 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)  
257/E21.001 .PROCESSES OR APPARATUS ADAPTED FOR MANUFACTURE OR TREATMENT OF SEMICONDUCTOR OR SOLID-STATE DEVICES OR OF PARTS THEREOF (EPO)  
257/E21.532 ..Manufacture or treatment of devices consisting of plurality of solid-state components formed in or on common substrate or of parts thereof; manufacture of integrated circuit devices or of parts thereof (EPO)  
257/E21.536 ...Manufacture of specific parts of devices (EPO)  
257/E21.54 ....Making of isolation regions between components (EPO)  
257/E21.545 ....Dielectric regions, e.g., EPIC dielectric isolation, LOCOS; trench refilling techniques, SOI technology, use of channel stoppers (EPO)  
257/E21.561 .....Using semiconductor or insulator technology, i.e., SOI technology (EPO)  
257/E21.564 .....SOI together with lateral isolation, e.g., using local oxidation of silicon, or dielectric or polycrystalline material refilled trench or air gap isolation regions, e.g., completely isolated semiconductor islands (EPO)

2 119/537 (1 OR, 1 XR)  
Class 119 ANIMAL HUSBANDRY  
119/416 .CONFINING OR HOUSING  
119/537 ..Portable or nonconfined bird perch

2 438/241 (2 OR, 0 XR)  
Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
438/142 .MAKING FIELD EFFECT DEVICE HAVING PAIR OF ACTIVE REGIONS SEPARATED BY GATE STRUCTURE BY FORMATION OR ALTERATION OF SEMICONDUCTIVE ACTIVE REGIONS  
438/197 ..Having insulated gate (e.g., IGFET, MISFET, MOSFET, etc.)  
438/238 ...Including passive device (e.g., resistor, capacitor, etc.)  
438/239 ....Capacitor  
438/241 ....And additional field effect transistor (e.g., sense or access transistor, etc.)

2 257/648 (0 OR, 2 XR)

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DIODES) Class 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE  
 257/629 .WITH MEANS TO CONTROL SURFACE EFFECTS  
 257/632 ..Insulating coating  
 257/647 ...Insulating layer recessed into semiconductor surface  
 (e.g., LOCOS oxide) 257/648 ....Combined with channel stop region in semiconductor

2 438/298 (0 OR, 2 XR)  
 Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
 438/142 .MAKING FIELD EFFECT DEVICE HAVING PAIR OF ACTIVE REGIONS  
 SEPARATED BY GATE STRUCTURE BY FORMATION OR ALTERATION OF SEMICONDUCTIVE ACTIVE  
 REGIONS  
 438/197 ..Having insulated gate (e.g., IGFET, MISFET, MOSFET, etc.)  
 438/294 ...Including isolation structure  
 LOCOS) 438/297 ....Recessed oxide formed by localized oxidation (i.e.,  
 438/298 .....Doping region beneath recessed oxide (e.g., to form  
 chanstop, etc.)

2 219/384 (0 OR, 2 XR)  
 Class 219 ELECTRIC HEATING  
 219/200 .HEATING DEVICES  
 219/383 ..Electric arc-type devices  
 219/384 ...With perforating or disintegrating means

2 438/452 (1 OR, 1 XR)  
 Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
 438/400 .FORMATION OF ELECTRICALLY ISOLATED LATERAL SEMICONDUCTIVE  
 STRUCTURE  
 438/439 ..Recessed oxide by localized oxidation (i.e., LOCOS)  
 438/452 ...Plural oxidation steps to form recessed oxide

2 119/468 (1 OR, 1 XR)  
 Class 119 ANIMAL HUSBANDRY  
 119/416 .CONFINING OR HOUSING  
 119/452 ..Animal display or open work enclosure (e.g., cage, etc.)  
 119/459 ...Bird cage  
 119/467 ....With attachment device  
 119/468 .....Perch for cage

2 438/294 (0 OR, 2 XR)  
 Class 438 SEMICONDUCTOR DEVICE MANUFACTURING: PROCESS  
 438/142 .MAKING FIELD EFFECT DEVICE HAVING PAIR OF ACTIVE REGIONS  
 SEPARATED BY GATE STRUCTURE BY FORMATION OR ALTERATION OF SEMICONDUCTIVE ACTIVE  
 REGIONS  
 438/197 ..Having insulated gate (e.g., IGFET, MISFET, MOSFET, etc.)  
 438/294 ...Including isolation structure

2 119/72.5 (2 OR, 0 XR)  
 Class 119 ANIMAL HUSBANDRY  
 119/72 .WATERING OR LIQUID FEED DEVICE  
 119/72.5 ..Drop-delivering